

FIG. 1

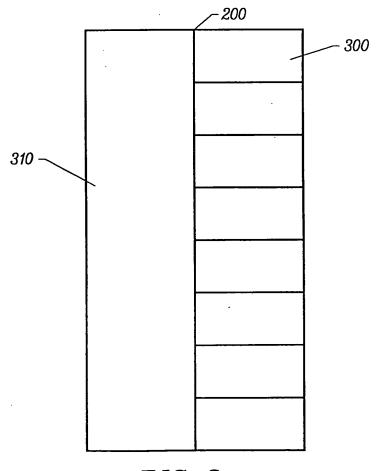


FIG. 3

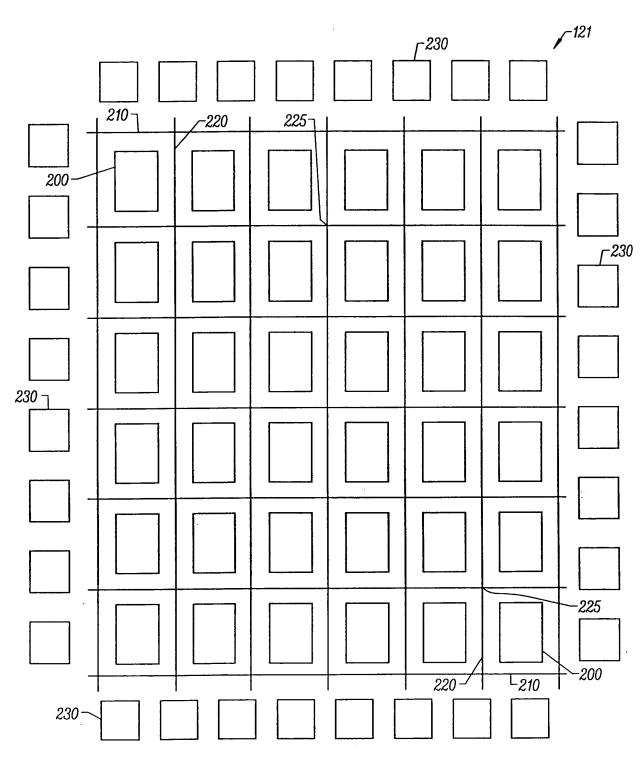


FIG. 2

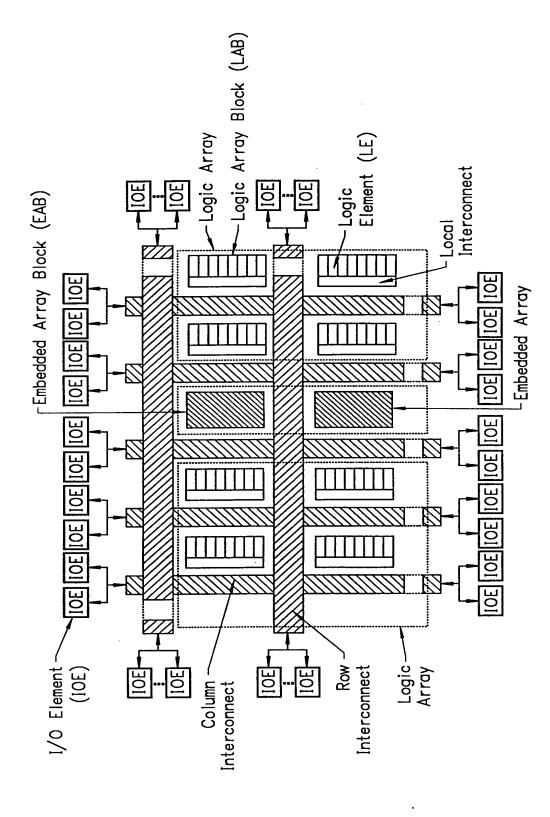


FIG. 4

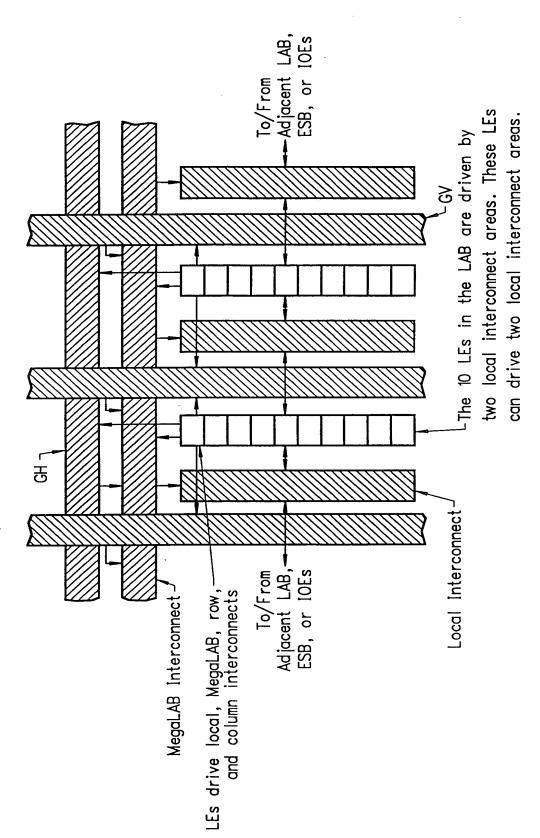
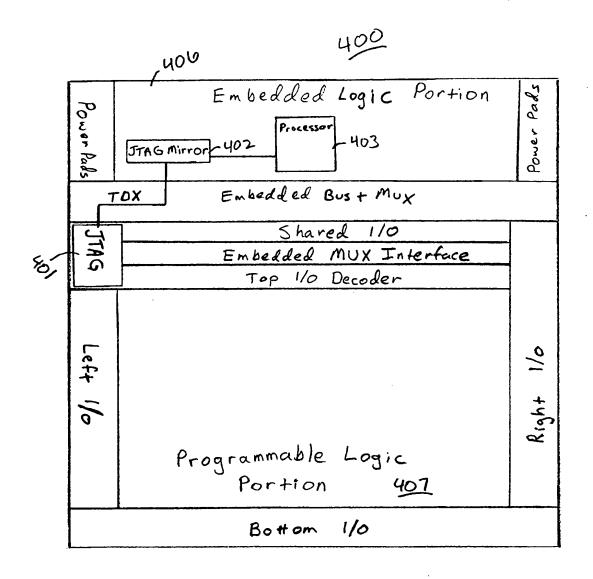
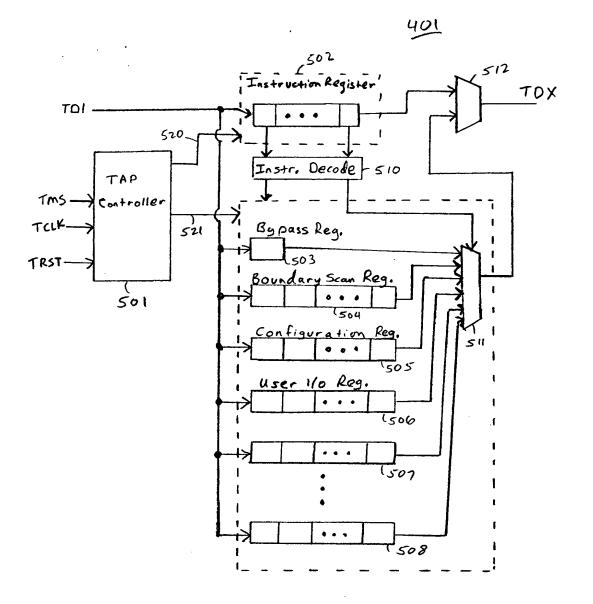


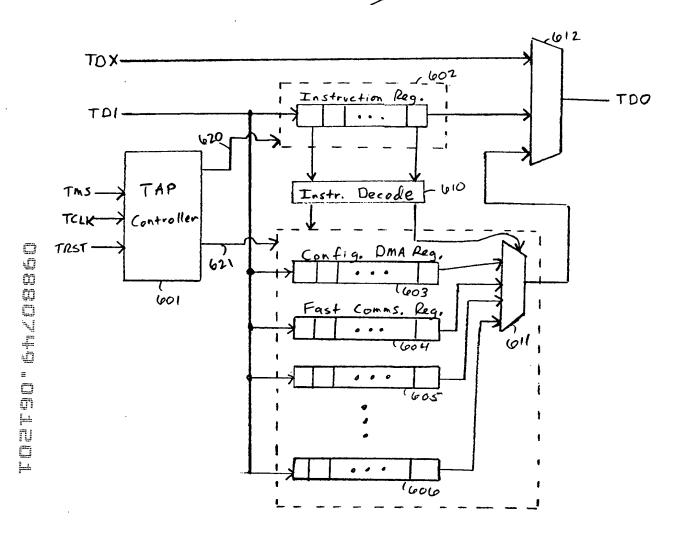
FIG. 5



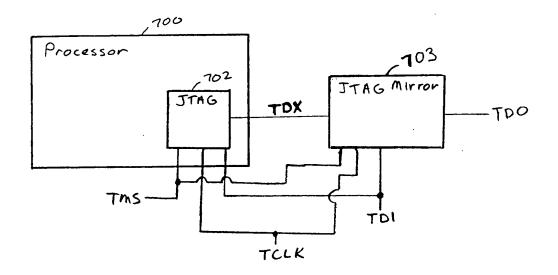
F1G. 6



F1G. 7



F16. 8



F1G. 9

WB 0 E ACTION ADDRESS DATA	last bit → 79 78 77 76	75 74 73 72 71	70 69	68 67 66 65 64	63 32	31 0 → first bit
	WB	0	Е	ACTION	ADDRESS	DATA

F16.10

31 30 29 28 27 26	6 25 24 23	22 21	20 19	18 17	16 1	5 14	13	12	11_	10	9	8	7	6	5	4	3	2	1	0
	•			0													P	С	BE	V.

FIG. 11

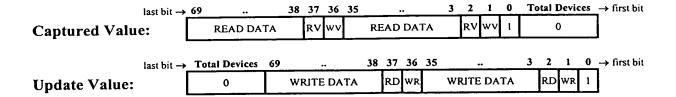


FIG. 12

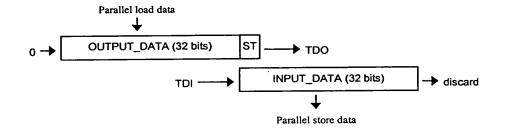


FIG. 13

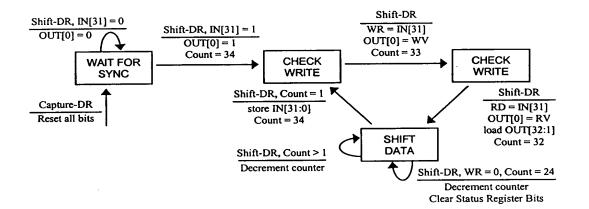


FIG. 14

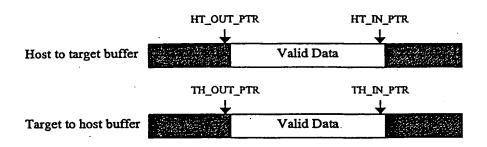


FIG. 15

31 30 29 28 2	7 26 25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
NR NB RB RE	RAVAIL		٧	/SP/	٩CE	=	WP									1								WE	WB	WF
<u> </u>			L				<u> </u>	<u> </u>																		

F1G. 16